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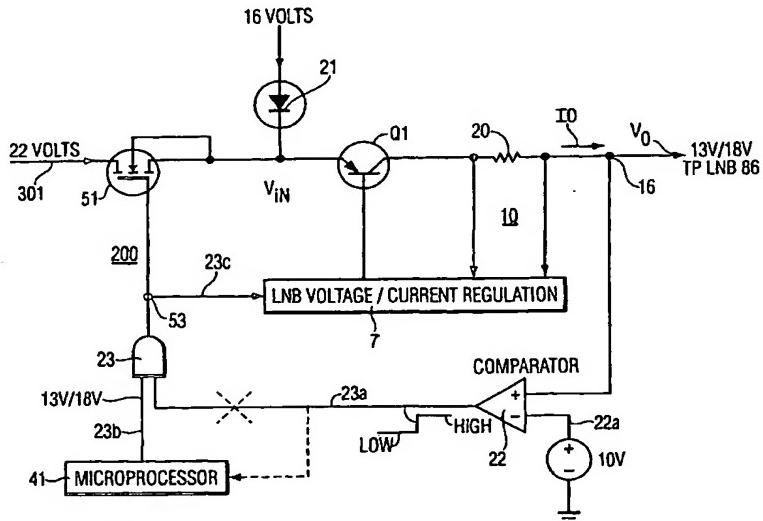
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(54) Title: POWER SUPPLY FOR A SATELLITE RECEIVER



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(57) Abstract: A power supply (10) for a satellite receiver system includes a dual input supply voltage arrangement (200). When a higher output voltage is selected, a source of a lower supply input voltage is coupled to an input main current conducting terminal of a series pass transistor (Q1). On the other hand, when a lower output voltage is selected, a source of a lower supply input voltage is coupled to the input main current conducting terminal of the series pass transistor. A comparator (22) senses a magnitude of an output voltage (16) produced by the series pass transistor. When, as a result of an over current condition, the output voltage is lower than a reference threshold level (22a), any selection of the higher output voltage is automatically over-ridden and the source of the lower supply input voltage, instead, is coupled to the input main current conducting terminal of the series pass transistor.



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INTERNATIONAL SEARCH REPORT

International application No.

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A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : H04B 1/06, 1/38
US CL : 455/572, 343.1, 230

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
U.S. : 455/572, 343.1, 230, 573, 8, 13.3, 13.4, 234.2, 250.1, 341, 343.2, 343.5, 127.1

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
NONE

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
USPAT, EPO, JPO, DERWENT : power supply, dual, satellite, control, fault detector.

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5,826,170 A (HIRSCHFIELD et al) 20 October 1998 (20.10.98), see entire document.	1-9
A	US 5,828,206 A (HOSONO et al) 27 October 1998 (27.10.98), see entire document.	1-9
A	US 6,061,577 A (ANDRIEU et al) 09 May 2000 (09.05.00), see entire document.	1-9
A	US 5,563,500 A (MUTERSPAUGH) 08 October 1996 (08.10.96), see entire document.	1-9

Further documents are listed in the continuation of Box C. See patent family annex.

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